



09/28/00

ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u> </u> Q.A. <u> </u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/672650	CONT/PRIOR	CLASS 711 714	SUBCLASS 3	ART UNIT 2187 3	EXAMINER Chase
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Stephen Jordan
Preet Singh

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TITLE	APPLICANTS
1. <i>General</i>	
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Memory tester has memory sets configurable for use as error catch RAM, tag RAM's, buffer memories and stimulus log RAM.

PTO-2040
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ISSUING CLASSIFICATION															
ORIGINAL				CROSS REFERENCE(S)											
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)										
INTERNATIONAL CLASSIFICATION															

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